

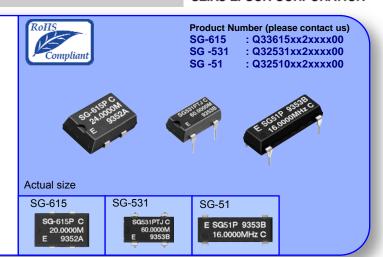
CRYSTAL OSCILLATOR (SPXO)

**OUTPUT: CMOS, TTL** 

SG-615 series SG-531/SG-51 series

•Frequency range : 1.025 MHz to 135 MHz •Supply voltage : 3.3 V Typ. / 5.0 V Typ.

 $\begin{array}{ll} \bullet \text{Function} & : \quad \text{Output enable(OE) or Standby(} \, \overline{\text{ST}} \, ) \\ \bullet \text{Pin compatible with full-size metal can. (SG-51 series)} \\ \bullet \text{Pin compatible with half-size metal can. (SG-531 series)} \\ \end{array}$ 



#### Specifications (characteristics)

		Specifica	Conditions / Remarks	
Item	Symbol	SG-615P SG-615PTJ SG-531P SG-531PTJ SG-51P SG-51PTJ		
Output frequency range	fo	1.025 MHz to 26 MHz 26.001 MHz to 66.667 MHz		Please contact us about available frequencies.
Supply voltage	Vcc	5.0 V ±0	0.5 V	
Storage temperature	T_stg	-55 °C to +	+125 °C	Storage as single product.
Operating temperature	T_use	-20 °C to	+70 °C	
Frequency tolerance	f_tol	$B^{*1}$ : $\pm 50 \times 10^{-6}$ , (	C: ±100 × 10 <sup>-6</sup>	-20 °C to +70 °C
Current consumption	Icc	23 mA Max.	35 mA Max.	No load condition
Disable current	I_dis	12 mA Max.	28 mA Max.	OE=GND
Symmetry	SYM	40 % to 60 %	_	CMOS load:50 % Vcc level
Symmetry	STIVI	40 % to 60 %	45 % to 55 %	TTL load: 1.4 V level
Output voltage	Vон	Vcc-0.4 V Min.	2.4 V Min.	Іон=-400 μА
	Vol	0.4 V Max.		IoL=16 mA(P)/ 8 mA(PTJ)
Output load condition (TTL)	L_TTL	10 TTL Max.	5 TTL Max.	L_CMOS ≤ 15 pF
Output load condition (CMOS)	L_CMOS	50 pF Max.	_	
	ViH	2.0 V Min.	3.5 V Min.	I <sub>IH</sub> = 1 μA Max. (OE=Vcc)
Input voltage	VIL	0.8 V Max.	1.5 V Max.	IIL= -100 μA Min. (OE=GND), PTJ:IIL= -500 μA Min.(OE=GND)
Rise time / Fall time	tr / tr	8 ns Max.	_	CMOS load:20 % Vcc to 80 % Vcc level
		8 ns Max.	5 ns Max.	TTL load:0.4 V to 2.4 V level
Start-up time	t_str	4 ms Max.	10 ms Max.	Time at minimum supply voltage to be 0 s
Frequency aging	f_aging	$\pm 5 \times 10^{-6} / y$	±5 × 10 <sup>-6</sup> / year Max.	

<sup>\*1 &</sup>quot;B" tolerance will be available up to 55 MHz.

### Specifications (characteristics)

Specifications (characteristics)							
	Symbol	Specifications					
Item		SG-615PCG SG-531PCG	SG-615SCG SG-531SCG	SG-615PCN	Conditions / Remarks		
Output frequency range	fo	1.500 MHz to 26.000 MHz		26.001 MHz to 66.667 MHz	Please contact us about available frequencies.		
Supply voltage	Vcc	2.7 V	to 3.6 V	3.0 V to 3.6 V			
Storage temperature	T_stg		-55 °C to +125 °C		Storage as single product.		
Operating temperature	T_use	-40 °C to +85 °C					
	f_tol	B: ±50 × 10 <sup>-6</sup> C: ±100 × 10 <sup>-6</sup>			-20 °C to +70 °C		
Frequency tolerance		M: ±100 × 10 <sup>-6</sup>			-40 °C to +85 °C		
Current consumption	Icc	12 mA Max.		20 mA Max.	No load condition		
Disable current	I_dis	10 mA Max.	10 mA Max. —		OE=GND (PCG,PCN)		
Stand-by current	I_std	— 50 μA Max.		_	ST =GND (SCG)		
Symmetry	SYM	45 % to 55 %		50 % Vcc level, L_CMOS=Max.			
Output voltage	Vон	Vcc-0.4 V Min.		Vcc-0.4 V Min.	Іон=-8 mA		
	Vol	0.4 V Max.		0.4 V Max.	IoL= 8 mA		
Output load condition	L_CMOS	25 pF Max.		15 pF Max.			
Input voltage	ViH	70 % Vcc Min.		70 % Vcc Min.	OE Terminal or ST Terminal		
	VIL	20 % Vcc Max.		30 % Vcc Max.			
Rise time / Fall time	tr / tf	4 ns Max.		20 % Vcc to 80 % Vcc level, L_CMOS ≤ Max.			
Start-up time	t_str	12 ms Max.		10 ms Max.	t=0 at 90% Vcc		
Frequency aging	f_aging	$\pm 5 \times 10^{-6}$ / year Max.		+25 °C, Vcc=3.3 V, First year			

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#### Specifications (characteristics)

Specifications						
Item	Symbol	SG-615PTW / STW	SG-615PHW / SHW	SG-615PCW / SCW	Conditions / Remarks	
		SG-531PTW / STW	SG-531PHW/SHW	SG-531PCW / SCW		
Output frequency range	fo	55.001 MHz to	135.000 MHz	26.001 MHz to 135.000 MHz	Please contact us about available frequencies.	
Supply voltage	Vcc	5.0 V	±0.5 V	3.3 V ±0.3 V		
Storage temperature	T_stg		-55 °C to +125 °C		Storage as single product.	
Operating temperature	T_use	-20 °C to	+70 °C	-40 °C to +85 °C		
Fraguency telerance	f tol	B: ±50 × 10 <sup>-6</sup> , C <sup>2</sup> : ±100 × 10 <sup>-6</sup>			-20 °C to +70 °C	
Frequency tolerance	f_tol	_	_	M: $\pm 100 \times 10^{-6}$	-40 °C to +85 °C	
Current consumption	Icc	45 m/	A Max.	28 mA Max.	No load condition( Max. frequency range )	
Disable current	I_dis	30 mA	A Max.	16 mA Max.	OE=GND (PTW,PHW,PCW)	
Stand-by current	I_std	50 μA Max.		ST =GND (STW,SHW,SCW)		
Symmetry	SYM	— 40 % to 60 %		50 % Vcc level, L_CMOS=Max.		
Symmetry		40 % to 60 %		_	1.4 V level ,L_CMOS=Max.	
Output voltage	Vон	Vcc-0.4 V Min.			Iон=-16 mA(PTW,STW,PHW,SHW),-8 mA(PCW,SCW)	
Output voltage	Vol	0.4 V Max.			IoL= 16 mA(PTW,STW,PHW,SHW), 8 mA(PCW,SCW)	
Output load condition (TTL)	L_TTL	5 TTL Max.		_	f0 ≤ 90 MHz , Max.supply voltage	
Output load condition (CMOS)	L_CMOS	15 pF Max.		Max.frequency , Max.supply voltage		
Input voltage	VIH	2.0 V Min.		70 % Vcc Min.	OE Terminal or ST Terminal	
Input voltage	VIL	0.8 V Max.		20 % Vcc Max.		
Rise time / Fall time	tr / tf	_	4 ns	Max.	20 % Vcc to 80 % Vcc level, L_CMOS ≤ Max.	
Nise time / Fail time		4 ns Max.		_	0.4 V to 2.4 V level	
Start-up time	t_str	10 ms Max		Time at minimum supply voltage to be 0 s		
Frequency aging	f_aging	$\pm 5 \times 10^{-6}$ / year Max.		+25 °C, Vcc=5.0 V / 3.3 V, First year		

<sup>\*2 &</sup>quot;C" tolerance : fo ≥66.667 MHz(PTW,STW,PHW,SHW)

Product Name (Standard form) SG-615 P C G 20.000000MHz C 1 4

①Model ②Function (P: Output enable, S:Standby)

③Supply voltage ④Frequency

⑤Frequency tolerance

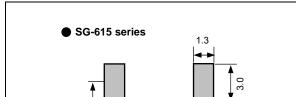
③Supply voltage			
С	3.3 V Typ.		
T,H	5.0 V Typ.		
Blank	5.0 V Typ.		

⑤Frequency tolerance				
В	±50 × 10 <sup>-6</sup> / -20 to +70°C			
С	±100 × 10 <sup>-6</sup> / -20 to +70°C			
М	±100 × 10 <sup>-6</sup> / -40 to +85°C			

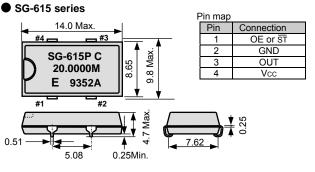
(Unit:mm)

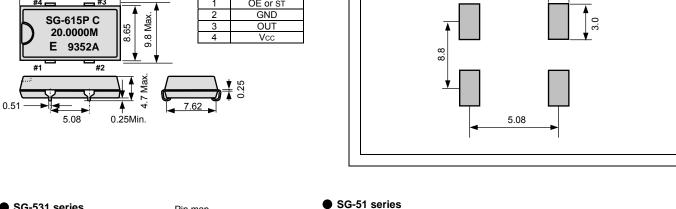
#### External dimensions

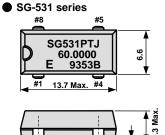
#### (Unit:mm)

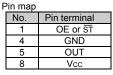


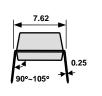
Footprint (Recommended)

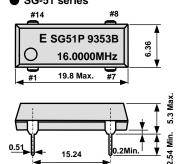




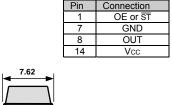








15.24



Pin map

			Ni .	
Note.				
OE pin (P,P)	TJ,PTW,PH	HW,PCW	,PCN,PCG)	1
OE pin = "H'	or "open"	: Specifi	ed frequenc	y outpu
OF nin = "I "	· Output is	s high im	nedance	

ST pin (STW, SHW, SCW,SCG)

ST pin = "H" or "open" : Specified frequency output.
ST pin = "L" : Output is low level

(weak pull - down), oscillation stops.

To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

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At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

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►Pb free.



- ► Complies with EU RoHS directive.
  - \*About the products without the Pb-free mark.

    Contains Pb in products exempted by EU RoHS directive.

    (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc ).

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