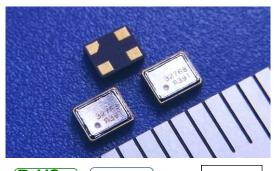
## **SMD Crystal Oscillator**

# FCXO-05D









#### **+ FEATURES**

- AT-cut crystal oscillator / 32.768 kHz.
- $2.5 \times 2.0 \times 0.9$  mm Max. /13 mg.
- Frequency tolerance ±7 ppm available.
- Operating supply current 0.01 mA Max. (the lowest in its class).
- Ceramic with metal lid sealed by patented Electron-Beam-Soldering.

#### **APPLICATIONS**

Smart-meters, wireless-modules.

#### ◆ STANDARD SPECIFICATIONS / ORDERING INFORMATION

X5D 32768 C Q3 Ordering Number (Sample): (2) (3)(1)

(1) Type				
X5D				

(2) Nominal	Frequency
32.768 kHz	32768

(3) Supply Voltage				
1.8 ±0.18 V 18				
2.5 ±0.25 V	25			
3.3 ±0.33 V	33			
Other: 1.60 ~ 3.63 V NN				

(4) Frequency Tolerance @ 25°C				
±7 ppm	Α	±20 ppm	D	
±10 ppm	В	±30 ppm	E	
±15 ppm	С	±50 ppm	F	
		Other	N	

#### 1/10 of the tolerance of typical tuning-fork oscillators

(5)		Frequency Temperature Characteristics				
Operating	↓	(with reference to 25°C)				
Temperature	±10 ppm	±10 ppm   ±15 ppm   ±20 ppm   ±30 ppm   ±50 ppr				
-20 ~ +70°C	P1	P2	P3	P4	P5	
-30 ~ +85°C	Q1	Q2	Q3	Q4	Q5	
-40 ~ +85°C	-	R2	R3	R4	R5	
Other	NN					

(6) Storage Temperature*1				
-40 ~ +85°C G				
-40 ~ +105°C	Н			
-55 ~ +125°C J				
Other N				
*1 Not applicable to packing materials				

(7) Tape & Reel (φ180 mm)				
3000 pcs/reel X				
Other	N			

Not applicable to packing materials

(8)	RIVER Use Only
	(As needed)

Common Parameter	Specification	Unit	Note
Operating Supply Current	0.01 Max.	mA	F = 32.768 kHz, VDD = 3.0V, No load
Stand-by Supply Current	3 Max.	μΑ	Stand-by = "L"
High-level Output Voltage	0.9VDD Min.	٧	Іон = -1 mA
Low-level Output Voltage	0.1VDD Max.	٧	IoL = +1 mA
Output Load	15 Max.	рF	-
Output Level	смоѕ	-	-
Duty Cycle	50 ±5	%	-
Rise / Fall Time	200 Max.	ns	10% VDD to 90% VDD level

#### 1/100 of the startup time of typical tuning fork oscillators

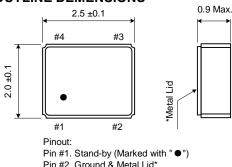
Common Parameter			Specification	Unit	Note
Startup Time		1	7.0 Max.	ms	V <sub>DD</sub> = 3.3 V
			10.0 Max.	ms	V <sub>DD</sub> = 1.8 V
(High)			0.7VDD Min.	٧	Output (pin #3) enabled
Stand-by (pin #1) Function	(Low)		0.3VDD Max.	v	Output (pin #3) disabled: High-Z

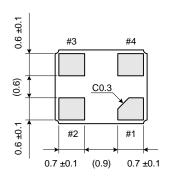
- The codes for the Ordering Number are indicated in blue, and the specifications are described in black.
- Not all combinations of options are available as standard.
- For specifications that include "Overall Frequency-Tolerance", please select "N" for the (4) Frequency Tolerance and let us know your specific requirements.
- For specifications other than those above, please contact our sales / website and let us know your specific requirements.

#### **OUTLINE DEMENSIONS**

Pin #3. Output

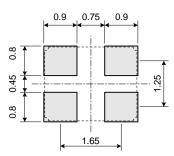
Pin #4. Vpp





### **◆ LAND PATTERN**

Unit: mm



 $\bullet$  For operational stability, a 0.01  $\mu\text{F}$  bypass capacitor should be placed between VDD (Pin #4) and GND (Pin #2) as close as possible to the product.

