

LV96/LV98 Series 3.3 V LVDS Clock Oscillators



- Pletronics LV96/LV98 Series is a quartz crystal controlled precision square wave generator with an LVDS output.
- Solder pad compatible legacy LVDS oscillator solutions.
- FR4 base using the LV93 or LV99 5x7 mm ceramic packaged SMD device.
- Tape and Reel packaging is available.

- 10.9 to 670 MHZ
- Enable/Disable Function: LV98 on pad 2 LV96 on pad 1
- Low Jitter

This series, LV96 and LV98, is not recommended for new designs. * For new designs, pin-out on pad 1 is the only available option for LV99 series part.

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.40 grams Moisture Sensitivity Level: 1 As defined in J-STD-020C Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{cc} Supply Voltage	-0.5V to +6.5V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V

Thermal Characteristics

The maximum die or junction temperature is 155°C The thermal resistance junction to board is 40 to 80°C/Watt depending on the solder pads, ground plane and construction of the PCB.

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Mechanical:





FR4 PCB Base: Solder masked

Label: laser marked lettering

All via holes tented on bottom Copper Clad 670 μ inch (17 μ m) Nickel plated 118 μ inch (3 μ m) Gold plated 0.8 μ inch (0.02 μ m) Typical thicknesses

Pin 3 Ground plane is typical **Not to scale**

	Inches	mm		
В	0.356 <u>+</u> 0.005	9.04 <u>+</u> 0.13		
С	0.126 <u>+</u> 0.005	3.21 <u>+</u> 0.13		
D	0.324 <u>+</u> 0.005	8.23 <u>+</u> 0.13		
F^1	0.050	1.27		
G¹	0.040	1.02		
H^1	0.059	1.50		
I^1	0.020	0.51		
J^1	0.040	1.02		
K ¹	0.100	2.54		
L1	0.062	1.57		

LV98 Pad	LV96 Pad	Function	Note
2	1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. This is not a recommended condition!!!!!! When this pad is <0.80 volts, the output will be inhibited (High impedance state) Recommend connecting this pad to V_{cc} if the oscillator is to be always on.
1	2	No function	Recommend connecting this pad to ground. The is internal connection.
:	3	Ground (GND)	
4	4	Output	The outputs must be terminated, 100 ohms between the outputs is the ideal
Ę	5	Output*	cermination. Capacitor coupled terminations can be used.
6		Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

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